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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hirohisa SAITO, et al.

Application No.: 10/724,603

Filed: December 02, 2003

Customer Number: 20277

Confirmation Number: 2606

Group Art Unit: 2822

Examiner: David E. Graybill

For: PACKAGE FOR HOUSING SEMICONDUCTOR CHIP, FABRICATION METHOD THEREOF AND SEMICONDUCTOR DEVICE

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is a Response To Restriction Requirement in the above-identified application.

- ☒ No additional fee is required.  
☐ Applicant is entitled to small entity status under 37 CFR 1.27  
☐ Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	14	20	0	\$50.00 =	\$0.00
Independent Claims	4	4	0	\$200.00 =	\$0.00
Multiple dependent claims newly presented					\$0.00
Fee for extension of time					\$0.00
					\$0.00
Total of Above Calculations					\$0.00

- ☐ Please charge my Deposit Account No. 500417 in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.
- ☒ The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMOTT WILL &amp; EMERY LLP



Brian K. Seidleck  
Registration No. 51,321

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
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Facsimile: 202.756.8087  
Date: July 5, 2005

Please recognize our Customer No. 20277 as our  
correspondence address.

Docket No.: 050389-0048



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
Hirohisa SAITO, et al.	:	Confirmation Number: 2606
Application No.: 10/724,603	:	Group Art Unit: 2822
Filed: December 02, 2003	:	Examiner: David E. Graybill
For: PACKAGE FOR HOUSING SEMICONDUCTOR CHIP, FABRICATION METHOD THEREOF AND SEMICONDUCTOR DEVICE		

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Noting the Office Action of June 17, 2005 wherein restriction has been required, Applicant hereby elects Group I (claims 1-10) for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP

A handwritten signature in black ink, appearing to read "Brian K. Seidleck".

Brian K. Seidleck  
Registration No. 51,321

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
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**Date: July 5, 2005**

**Please recognize our Customer No. 20277 as  
our correspondence address.**